

International Photonics & Electronics Committee

400G-eSR8 Implementation Agreement

IPEC-400G-eSR8 V1.0

IPEC PMD WG
www.ipec-std.org





400G-eSR8 Implementation Agreement

Version 1.0

August 23, 2025

Implementation Agreement created and approved**IPEC****www.ipec-std.org**

The International Photonics & Electronics Committee (IPEC) is an international standards organization that is committed to developing open optoelectronic standards and delivering strategic roadmap reports. IPEC focuses on standardizing solutions in optical chips, optical/electrical components, and optical modules. Markets addressed by IPEC include 5G, IoT and AI. The gradual digitalization of these industries and the construction of new infrastructure require standardization. However, current optoelectronic standards are reactive, do not proactively motivate strategic investments, and do not address all market segments. Therefore, IPEC will look beyond the work of other standards organizations. IPEC will support the optoelectronic industry for the long term, and address un-met needs of its members.

IPEC is open to any interested party who wishes to join. All members have the opportunity to participate in developing all IPEC standards and reports on a non-discriminatory basis. The process of developing these standards and reports is transparent to all members. IPEC is registered in Switzerland.

400G-eSR8 Implementation Agreement

▪ Summary

The objective of this document is to define one 400 Gb/s PMD for operation over multi-mode fibers (MMF) with lengths up to at least 200 m.

▪ Keywords

400G, eSR, PAM4

Working Group: PMD

TITLE: 400G-eSR8 Implementation Agreement

SOURCE: TECHNICAL EDITOR

Name: Mingwang Mao

Company Name: Meituan

Email: maomingwang@meituan.com

SOURCE: TECHNICAL EDITOR

Name: Limin Geng

Company Name: Huawei Technologies Co., Ltd

Email: genglimin@huawei.com

SOURCE: TECHNICAL EDITOR

Name: Guangcan Mi

Company Name: Huawei Technologies Co., Ltd

Email: miguangcan@huawei.com

SOURCE: TECHNICAL EDITOR

Name: Yuan Lu

Company Name: Senko

Email: John.Lu@senko.com

WORKING GROUP CHAIR

Name: Tom Issenhuth

Company Name: Huawei Technologies Co., Ltd

Email: tissenhuth@outlook.com

▪ List of Members

The following companies were members of this project at the release of this specification:

- Accelink Technologies Co., Ltd.
- Acon Optics
- Advanced Fiber Resources (Zhuhai), Ltd.
- Amphenol Corporation
- Applied Optoelectronics, Inc.
- ATOP Corporation
- AVIC Jonhon Optronic Technology Co., Ltd.
- Broadex Technologies Co., Ltd.
- China Academy of Information and Communications Technology
- China Telecommunications Corporation
- CIG Shanghai Co., Ltd.
- FiberHome Telecommunication Technologies Co., Ltd.
- Fujin Precision Industrial (Jincheng) Co., Ltd.
- Fujitsu Optical Components, Ltd.
- HG Genuine Optics Tech Co., Ltd.
- Hisense Broadband Multimedia Technologies Co., Ltd.
- Huawei Technologies Co., Ltd.
- InnoLight Technology (Suzhou) Ltd.
- Intel Corporation
- Liobate Technologies Limited
- LuxshareTech

- Marvell
- Meituan
- National Optoelectronics Innovation Center (NOEIC)
- New H3C Technologies Co., Ltd.
- Net Communications (Shenzhen) Limited
- Semtech Semiconductor (Shenzhen) Company Limited
- Senko (Shenzhen)
- Shanghai Jiao Tong University
- SiFotonics Technologies
- Citrus Technology
- Source Photonics
- Suzhou GL Foresight Electronics Technology Co., Ltd.
- VIAVI
- Wenzhou Yihua Connector Co., Ltd.
- Aroptics-tech Co., Ltd.
- Mindsemi Company Limited
- Yamaichi Electronics Co., Ltd.
- Yangtze Optical Fibre and Cable Joint Stock Ltd.
- Yuanjie
- ZTE Corporation

▪ Revision History

Rev	Date	Description
D0.1	2024-11-19	Initial release of this IA.
D0.2	2025-03-17	Some editorial modifications.
D0.9	2025-04-23	Add description about optical interfaces, and some editorial modifications.
D1.0	2025-06-10	Draft Version 1.0
D1.1	2025-07-16	Draft Version 1.1
V1.0	2025-08-23	Released after IPEC's BoD approval.

Table of Contents

Table of Contents

1	Introduction.....	9
1.1	Scope	9
1.2	Hardware Signaling Pins.....	9
1.3	Module Management Interface	9
1.4	High-Speed Electrical Characteristics	9
1.5	FEC Requirements	9
1.6	Mechanical Dimensions.....	9
2	400G-eSR8 Optical Specifications.....	10
2.1	400G-eSR8 Illustrative Link Power Budget	12
3	Definition of Optical Parameters and Measurement Methods	12
3.1	Test Patterns for Optical Parameters.....	12
3.2	Skew and Skew Variation.....	12
3.3	Wavelength and Spectral Width.....	12
3.4	Average Optical Power	13
3.5	Optical Modulation Amplitude (OMA _{outer})	13
3.6	Transmitter and Dispersion Eye Closure for PAM4 (TDECQ)	13
3.6.1	TDECQ Reference Equalizer	13
3.7	Extinction Ratio	13
3.8	Transmitter Transition Time.....	13
3.9	Relative Intensity Noise (RIN _{OMA}).....	13
3.10	Receiver Sensitivity.....	13
3.11	Stressed Receiver Sensitivity.....	13
4	Fiber Optic Cabling Model.....	13
5	Characteristics of the Fiber Optic Cabling (Channel)	14
6	Optical Interfaces.....	14
7	References.....	15
7.1	Normative References.....	15
8	Appendix A: Abbreviations and Acronyms	15

▪ **List of Figures**

Figure 1	400G-ESR8 module block diagram.....	10
Figure 2	400G-ESR8 optical link budget and sensitivity mask.....	12
Figure 3	Fiber optic cabling model.....	13
Figure 4	MPO-16 MDI	14
Figure 5	MPO-12 Two Row MDI	14
Figure 6	MPO-16 One row optical connector and module receptacle	15
Figure 7	MPO-12 Two rows optical connector and module receptacle	15

▪ **List of Tables**

Table 1	400G-ESR8 operating range.....	10
Table 2	400G-ESR8 optical transmit characteristics	10
Table 3	400G-ESR8 optical receive characteristics	11
Table 4	400G-ESR8 optical link budget.....	11
Table 5	Fiber optic cabling characteristics (channel) for 400G-ESR8.....	14

1 Introduction

This Implementation Agreement (IA) specifies physical layer specifications and parameters for 400 Gb/s optical interfaces.

The objective of this document is to define a 400 Gb/s Physical Medium Dependent (PMD) sublayer for operation over multi-mode fibers (MMF) with lengths up to at least 200 m.

1.1 Scope

This IA defines an optical interface for 400 Gb/s optical transceivers in Ethernet applications. Forward error correction (FEC) is required to be implemented by the host to ensure reliable system operation. The transceiver communicates over MMF of lengths from 2 meters up to at least 200 meters. The transceiver's electrical interface is not specified by this IA but can have, for example, eight lanes in each direction, with a nominal signaling rate of 53.125 Gb/s per lane.

A variety of form factors for the transceivers are possible and none are precluded by this IA.

1.2 Hardware Signaling Pins

Hardware signaling pins are specified in the multi-source agreements (MSAs) of respective module form factors.

1.3 Module Management Interface

The contents of the various ID registers shall comply with the requirements of the module MSAs.

1.4 High-Speed Electrical Characteristics

The detailed high-speed electrical characteristics are not defined by this IA. 400GE modules should be implemented in compliance with applicable electrical interface specifications.

1.5 FEC Requirements

400GE links rely on the host system implementing the 400GBASE-R PCS layer in accordance with clauses from the IEEE Std 802.3-2022™ and IEEE Std 802.3ck-2022™.

1.6 Mechanical Dimensions

Mechanical dimensions are defined in the MSA specifications of module form factors. This IA does not specify the use of a specific form factor.

Figure 1 shows the block diagram of a 400G eSR8 module with eight optical transmitters and eight optical receivers.

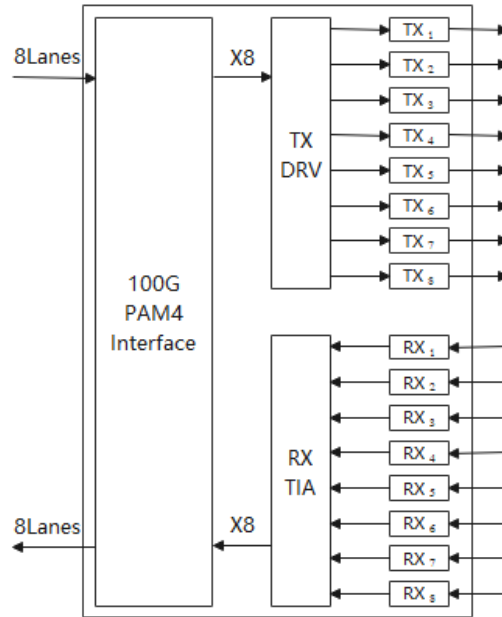


Figure 1 400G-eSR8 module block diagram

2 400G-eSR8 Optical Specifications

This IA defines the 400G-eSR8 optical interface for 400 Gb/s optical transceivers. The following tables show the common parameters: operating range, optical transmit characteristics, 400G-eSR8 optical receive characteristics and optical link budget.

Table 1 400G-eSR8 operating range

PMD Type	Required Operating Range
400G-eSR8	2 m–200 m for OM4

Table 2 400G-eSR8 optical transmit characteristics

Description	Value	Unit
Signaling rate (range)	26.5625±100 ppm	GBd
Modulation format	PAM4	-
Wavelength (range)	840 to 860	nm
RMS spectral width	0.55	nm
Average launch power, each lane (max)	4	dBm
Average launch power, each lane (min)	-6.5	dBm
Outer optical modulation amplitude (OAMouter) (max)	3	dBm
Outer optical modulation amplitude (OMAouter) (min) for TDECQ < 1.4 dB	-3.5 -4.9+TDECQ	dBm

for $1.4 \text{ dB} \leq \text{TDECQ} \leq 3.9 \text{ dB}$		
Transmitter and dispersion eye closure for PAM4 (TDECQ) (max)	3.9	dB
TDECQ- $10\log_{10}(\text{Ceq})$, each lane (max)	3.9	dB
Average launch power of OFF transmitter (max)	-30	dBm
Extinction ratio (min)	3.0	dB
Transmitter transition time (max)	34	ps
$\text{RIN}_{12\text{OMA}}$ (max)	-128	dB/Hz
Optical return loss tolerance (max)	12	dB
Encircled flux, each lane	$\geq 86\% @ 19 \mu\text{m}$ $\leq 30\% @ 4.5 \mu\text{m}$	

Table 3 400G-eSR8 optical receive characteristics

Description	Value	Unit
Signaling rate (range)	$26.5625 \pm 100 \text{ ppm}$	GBd
Modulation format	PAM4	-
Wavelengths (range)	840 to 860	nm
Damage threshold, each lane	5	dBm
Average receive power, each lane(max)	4	dBm
Average receive power, each lane (min)	-8.4	dBm
Receive power (OMAouter), each lane (max)	3	dBm
Receiver reflectance (max)	-12	dB
Receiver sensitivity (OMAouter) (max)	$\text{RS} = \max(-6.5, \text{SECQ} - 7.9)$	dBm
Stressed receiver sensitivity (OMAouter) (max)	-3.4	dBm
Conditions of stressed receiver sensitivity test:		
Stressed eye closure for PAM4 (SECQ)	4.5	dB
$\text{SECQ} - 10\log_{10}(\text{Ceq})f$ (max), lane under test	4.5	dB
OMAouter of each aggressor lane	3	dBm

Table 4 400G-eSR8 optical link budget

Description	Value	Unit
Effective modal bandwidth at 850 nm^a	4700	MHz.km

Power budget (for max TDECQ):	6.9	dB
Operating distance	200	m
Channel insertion loss ^b	2.4	dB
Maximum discrete reflectance	-35	dB
Allocation for penalties (for max TDECQ):	3.9	dB
Additional insertion loss allowed	0.6	dB

^a Per IEC 60793-2-10.

^b The channel insertion loss is calculated using the maximum distance specified in Table 1 with a cabled optical fiber attenuation of 3.5 dB/km at 850 nm plus a 1.7 dB allocation for total connection and splice loss.

2.1 400G-eSR8 Illustrative Link Power Budget

Figure 2 shows 400G-eSR8 optical link budget and sensitivity mask.

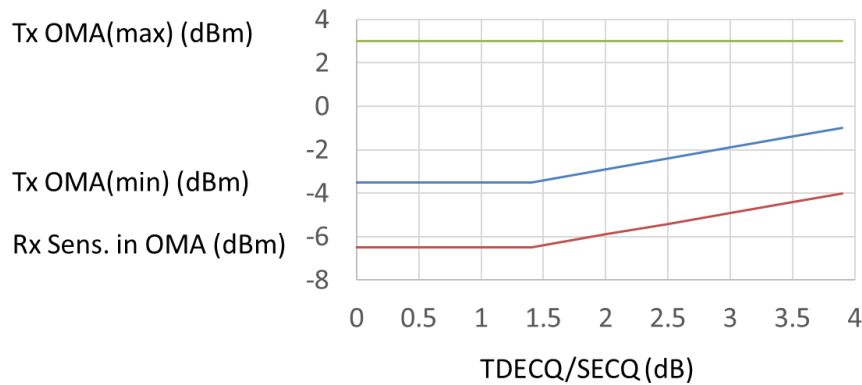


Figure 2 400G-eSR8 optical link budget and sensitivity mask

3 Definition of Optical Parameters and Measurement Methods

The specific test patterns are defined in Table 138-11 of IEEE Std 802.3-2022 [1].

3.1 Test Patterns for Optical Parameters

Detailed information on the test patterns for optical parameters is available in Clause 138.8 of IEEE Std 802.3-2022 [2].

3.2 Skew and Skew Variation

The skew and skew variation are available in Clause 138.3 of IEEE Std 802.3-2022 [1].

3.3 Wavelength and Spectral Width

The center wavelength and RMS spectral width of each optical lane shall be within the range given in Table 3 if measured per IEC 61280-1-3. The lane under test should be modulated using test patterns specified in Clause 3.1.

3.4 Average Optical Power

The specific average optical power shall be within the limit given in Table 2 if measured per IEC 61280-1-1 and using one of the test patterns specified in Clause 3.1.

3.5 Optical Modulation Amplitude (OMA_{outer})

The specific definition of OMA_{outer} is defined in Clause 121.8.4 of IEEE Std 802.3-2022 [1].

3.6 Transmitter and Dispersion Eye Closure for PAM4 (TDECQ)

The methodology of TDECQ is specified in Clause 167.8.6 of IEEE Std 802.3db-2022 [2].

3.6.1 TDECQ Reference Equalizer

The TDECQ reference equalizer is defined in Clause 167.8.7 of IEEE Std 802.3db-2022 [2].

3.7 Extinction Ratio

The test method of extinction ratio is defined in Clause 121.8.6 of IEEE Std 802.3-2022 [1]. The extinction ratio of each lane shall be compliant to the limit listed in Table 2.

3.8 Transmitter Transition Time

The test method of transmitter transition time is defined in Clause 138.8.7 of IEEE Std 802.3-2022 [1]. The transmitter transition time of each lane shall be compliant to the limit listed in Table 2.

3.9 Relative Intensity Noise (RIN_{12OMA})

The test method of RIN_{12OMA} is defined in Clause 138.8.8 of IEEE Std 802.3-2022 [1]. RIN_{12OMA} of each lane shall be compliant to the limit listed in Table 2.

3.10 Receiver Sensitivity

The test method of RIN_{12OMA} is defined in Clause 138.8.8 of IEEE Std 802.3-2022 [1]. RIN_{12OMA} of each lane shall be compliant to the limit listed in Table 2.

3.11 Stressed Receiver Sensitivity

The test method of stressed receiver sensitivity is defined in Clause 138.8.10 of IEEE Std 802.3-2022 [1].

4 Fiber Optic Cabling Model

The fiber optic cabling model of 400G-eSR8 is shown in Figure 3.

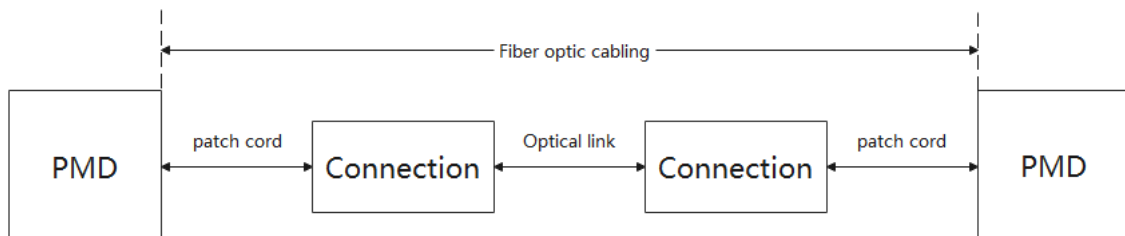


Figure 3 Fiber optic cabling model

5 Characteristics of the Fiber Optic Cabling (Channel)

The channel insertion loss is calculated using the maximum distance specified in Table 1 with a cabled optical fiber attenuation of 3.5 dB/km at 850 nm plus a 1.7 dB allocation for total connection and splice loss. The fiber optic cabling requirements for 400G-eSR8 are specified in Table 5

Table 5 Fiber optic cabling characteristics (channel) for 400G-eSR8

Description	400G-ESR8	Unit
Operating distance (max)	200	m
Channel insertion loss ^a (max)	2.4	dB
Cabling skew (max)	79	ns
Cabling skew variation	4.8	ns
Discrete reflectance (max)	-20	dB
Additional loss allowed	0.6	dB

^a The channel insertion loss includes connectors, splices, and more, and is over the wavelength range as specified in Table 2.

6 Optical Interfaces

This specification defines two mandatory Media Dependent Interfaces (MDIs). The spatial alignment of transmit (Tx) and receive (Rx) optical lanes shall comply with the geometric configurations defined in Figure 4, Figure 5 when viewed with the connector keying feature oriented in the upward position.

MPO-16

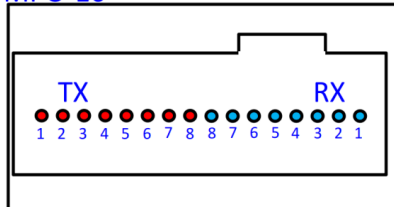


Figure 4 MPO-16 MDI

MPO-12

Two Row

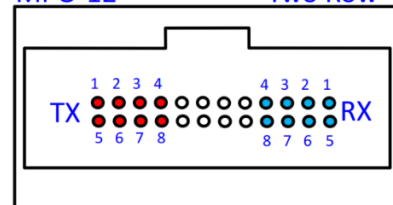


Figure 5 MPO-12 Two Row MDI

MPO (Multi-fiber Push-On) connectors shall utilize rectangular ferrules to align and secure optical fibers in standardized arrays (e.g., 12, 16, or 24 fibers see Figure 7&8&9). Connector keying and polarization shall comply with IEC 61754-7 and TIA-604-5 specifications, ensuring consistent Tx/Rx channel assignment per predefined fiber sequences.

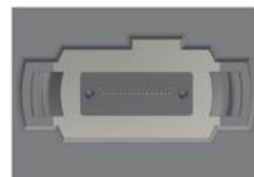


Figure 6 MPO-16 One row optical connector and module receptacle



Figure 7 MPO-12 Two rows optical connector and module receptacle

7 References

7.1 Normative References

1. IEEE Std 802.3-2022: IEEE Standard for Ethernet
2. IEEE Std 802.3db-2022: IEEE Standard for Ethernet Amendment 3: Physical Layer Specifications and Management Parameters for 100 Gb/s, 200 Gb/s, and 400 Gb/s Operation over Optical Fiber Using 100 Gb/s Signaling
3. IEEE Std 802.3ck-2022™ IEEE Standard for Ethernet Amendment 4: Physical Layer Specifications and Management Parameters for 100 Gb/s, 200 Gb/s, and 400 Gb/s Electrical Interfaces Based on 100 Gb/s Signaling

8 Appendix A: Abbreviations and Acronyms

The following abbreviations and acronyms are used in this IA:

DeMUX	Demultiplexer	PRBS	Pseudo-Random Bit Sequence
DGD	Differential Group Delay	QSFP	Quad Small Form Factor Pluggable
DRV	Driver	RIN	Relative Intensity Noise
FEC	Forward Error Correction	RX	Receiver
IA	Implementation Agreement	SECQ	Stressed Eye Closure for PAM4
MSA	Multisource Agreement	SMF	Single Mode Fiber
MUX	Multiplexer	SMSR	Side-Mode Suppression Ratio
OMA	Optical Modulation Amplitude	SSPRQ	Short Stress Pattern Random Quaternary
OMOuter	Outer Optical Modulation Amplitude	TDECQ	Transmitter and Dispersion Eye Closure for PAM4
PCS	Physical Coding Sublayer	TIA	Transimpedance Amplifier
PMA	Physical Medium Attachment	TX	Transmitter
PMD	Physical Medium Dependent		



Email: info@ipec-std.org

Website: www.ipec-std.org

COPYRIGHT© 2025 IPEC. All rights reserved.